

親愛的老師與研究團隊同仁：

2026 年度第三場「NSTCCore 高效能核心科學計算服務教育訓練工作坊」將於 2026 年 10 月 22 日（四）於中央研究院物理研究所（台北）舉行。

本工作坊由 國科會高效能核心科學計算服務計畫 支持，並由 中央研究院物理研究所網格計算中心（ASGC）執行。本次特別邀請 輝達（NVIDIA）技術講者，分享 AI、量子運算與高效能運算（HPC）的發展趨勢，協助研究團隊掌握最新技術方向，並介紹 NSTCCore 核心計算服務資源與應用案例。

誠摯邀請各大專院校與研究機構中，具科學計算與資料分析需求之研究團隊踴躍參與。

活動重點

- 協助研究團隊便利使用 NSTCCore 核心計算服務
- Quantum × GPU × AI：探索下一代超級運算
- NVIDIA Quantum-GPU Supercomputing：掌握最新技術與發展趨勢
- 促進產學交流與跨領域合作機會

活動資訊

- 時間：2026 年 10 月 22 日（四）下午 1:45 ~ 4:35
- 地點：[台北中央研究院物理研究所 1F 演講廳](#)
- 活動報名：<https://indico4.twgrid.org/event/75/>
- 報名截止：2026 年 10 月 18 日
- 活動海報： NSTCCore_3rdWorkshop_2026_AI_NVIDIA_Poster.pdf

補充說明

- 本次活動以中文進行，簡報資料提供中 / 英文版。
- 開放線上參與，請於報名時選擇參與方式。

如有任何疑問，請來信 DiCOS-Support@twgrid.org，期待與您在工作坊相見！

Dear Professors and Research Teams,

The **third NSTCCore High-Performance Computing (HPC) User Training Workshop of 2026** will be held on **Thursday, October 22, 2026**, at the **Institute of Physics, Academia Sinica, Taipei**.

This workshop is supported by the **NSTCCore HPC Service Project** and organized by the **Academia Sinica Grid Computing Centre (ASGC)**. An invited speaker from **NVIDIA** will share insights into the latest developments and trends in **AI, quantum computing, and high-performance computing (HPC)**, helping research teams stay informed about emerging technologies. The workshop will also introduce **NSTCCore computing services, resources, and application cases**.

Research teams from universities and research institutes with interests in **scientific computing and data analysis** are warmly invited to participate.

Workshop Highlights

- Overview of the NSTCCore program, services, resources, and collaboration models.
- Quantum × GPU × AI: Exploring the Next Generation of Supercomputing.
- NVIDIA Quantum-GPU Supercomputing: Exploring the Latest Technologies and Emerging Trends.
- Foster academia–industry exchange and opportunities for cross-disciplinary collaboration.

Event Information

- Date & Time: **1:45 ~ 4:35PM, Thursday, October 22, 2026**
- Venue: **1F Auditorium, Institute of Physics, Academia Sinica, Taipei**
- Event Registration: <https://indico4.twgrid.org/event/75/>
- Registration Deadline: **October 18, 2026**
- Event Poster:  NSTCCore_3rdWorkshop_2026_AI_NVIDIA_Poster.pdf

Notes

- The workshop will be conducted primarily in **Chinese**, with presentation **materials** provided in **Chinese or English**.
- **Online participation** is available. Please select the “Online Participation” option when registering.

For inquiries, please contact us at DiCOS-Support@twgrid.org. We sincerely look forward to your participation!